

RECEIVED  
CENTRAL FAX CENTER

NO. 662 P. 2

JUL 09 2004

OFFICIAL

Docket No. 2001-0092-1

USPN 10/036,676

Date of Deposit: July 9, 2004 "CERTIFICATE OF MAILING BY "FACSIMILE"

I hereby certify that this paper or fee is being sent via facsimile to (703) 872-9306 A First Class Mail Post Office to  
Addressee services under 37 C.F.R. 1.08 on the date indicated below and is addressed to the Commissioner for Patents,  
Washington, D.C., 20231.

Stephanie Shaver

(Typed or Printed Name of Person mailing papers or fees)

*Stephanie Shaver*

(Signature of Person mailing papers or fees)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Christian Wittak, et al.

Examiner: L. Al Nazer

Serial No.: 10/036,676

Group Art Unit: 2828

Filed: December 21, 2001

For: FOUR KHz GAS DISCHARGE LASER  
SYSTEMOK to  
enter.

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

*20*  
*2/21/05*AMENDMENT UNDER 37 CFR 1.312

Sir:

Applicants respectfully requests correction of claims 18, 19, and 21 with regard to formal matters without changing the scope of the claims for the above-captioned patent application. The changes required are of a clerical nature and do not add new matter. Specifically, claims 18, 19 and 21 are attached hereto with the appropriate bracketing and underlining along with the other pending claims.

Applicants respectfully submits this amendment is necessary for proper disclosure of the invention and these formal changes in claims 18, 19, and 21 will not require no additional

1 of 13

Docket No. 2001-0092-1  
USSN 10/036,676

least the first and the second fringe to the microprocessor.

67. (Original) The apparatus of claim 57 further comprising:

the programmable logic device containing logic to identify at least a first fringe comprising a selected fringe positioned within a first window in the array and a second fringe comprising a selected fringe positioned within a second window in the array and to identify at least the first and the second fringe to the microprocessor.

68. (Original) The apparatus of claim 58 further comprising:

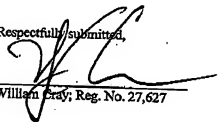
the programmable logic device containing logic to identify at least a first fringe comprising a selected fringe positioned within a first window in the array and a second fringe comprising a selected fringe positioned within a second window in the array and to identify at least the first and the second fringe to the microprocessor.

#### Conclusion

No new matter has been added.

Applicants do not believe any fees are due in connection with this submission however, if fees are due, the Commissioner is authorized to charge our Deposit Account Number 03-4060 the appropriate amount.

Respectfully submitted,

  
William Gray, Reg. No. 27,627

Cymer, Inc.  
17075 Thornmint Court  
Legal Department - MS/4-2C  
San Diego, California 92127-1712  
Telephone: (858) 385-7185  
July 8 2004

Customer No. 21773